

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1-7. (Canceled)

8. (Currently Amended) In a reconfiguration computing system including a reconfigurable computing chip having reconfigurable logic and multiple configuration planes, the system further including off-chip storage, a method of configuration management, comprising storing in a table a current state of the on-chip configurations, said table ~~consisting~~ comprising of multiple entries, each entry identifying an on-chip configuration plane and identifying a unique off-chip address of a loaded configuration.

9. (Previously Presented) The method of claim 8, further comprising:
saving the table, to save up to an entire state of the reconfigurable logic;
loading the table; and
from information stored in the table, loading the identified configurations into the identified on-chip configuration planes.

10. (Original) The method of claim 8, further comprising establishing initial boot conditions in the reconfigurable logic by:

specifying in the table an address of a boot configuration; and

automatically loading the boot configuration into the reconfigurable computing chip on boot up.

11. (Previously Presented) A method comprising:

populating a memory having a plurality of entries, one or more of such entries being associated with a configuration plane of an associated reconfigurable chip, such entries identifying an on-chip configuration plane and an associated off-chip address of a loaded configuration.

12. (Previously Presented) A method according to claim 11, further comprising:

selectively loading a configuration in a configuration plane; and
unloading an active configuration from an active configuration plane.

13. (Previously Presented) A method according to claim 12, further comprising:

activating the configuration plane including the loaded configuration.

14. (Previously Presented) A method according to claim 11, wherein the plurality of entries within a memory is associated with a table.

15. (Previously Presented) A method according to claim 14, further comprising:

saving the table to preserve up to an entire state of the reconfigurable chip:
loading the table, and

loading one or more identified configurations into one or more identified planes of the reconfigurable chip from information stored in the table.

16. (Previously Presented) A method according to claim 14, further comprising:
establishing one or more boot conditions in the reconfigurable logic.

17. (Previously Presented) A method according to claim 16, the element of establishing boot conditions comprising:
specifying in the table an address of a boot configuration, and
selectively loading the boot configuration into a configuration plane of the reconfigurable chip upon receiving an indication of a boot condition.

18. (Previously Presented) A system comprising:
a reconfigurable chip including reconfigurable logic and multiple configuration planes, the reconfigurable chip including a configuration table with multiple entries in which to store configuration content, wherein one or more of such entries are associated with a configuration plane and identify an off-chip address of a loaded configuration; and
a memory system, coupled with the reconfigurable chip, to store at least a subset of the configurations associated with configuration planes of the reconfigurable chip.

19. (Previously Presented) A system according to claim 18, wherein the memory system is one or more memory devices

20. (Previously Presented) A system according to claim 18, wherein the memory system is co-located with the reconfigurable chip within a system chassis.

21. (Previously Presented) A system according to claim 18, wherein the reconfigurable chip selectively saves the table to save up to an entire state of the reconfigurable logic, selectively loads the table, and selectively loads configuration information from the loaded table to configure one or more configuration planes comprising the reconfigurable chip.

22. (Previously Presented) A system according to claim 18, wherein at least a subset of entries in the table are associated with a boot configuration for the reconfigurable chip, and wherein a pointer to the boot configuration is set upon detection of a boot event to load the boot configuration into a configuration plane of the reconfigurable chip.